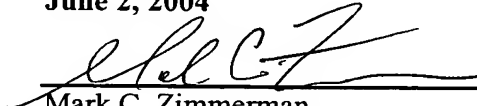


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PATENT  
Docket No. Intel/18683

IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE

|   |   |  |
|---|---|--|
| Applicants: Zimmer et al.                   | ) | I hereby certify that this paper is  |
|   | ) | being deposited with the United  |
| Serial No.: 10/807,668                      | ) | States Postal Service with   |
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|   | ) | Commissioner for Patents, P.O.   |
| For: Methods and Apparatus for Initializing | ) | Box 1450, Alexandria, VA 22313-  |
| a Memory                                    | ) | 1450 on this date:   |
|   | ) |  |
| Group Art Unit: Unknown                     | ) | June 2, 2004   |
|   | ) |  |
| Examiner: Unknown                           | ) |  |
|   | ) | Mark C. Zimmerman  |
|   | ) | Registration No. 44,006  |
|   | ) | Attorney for Applicant(s)  |

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the publications are enclosed.

**TIME OF FILING**

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

## METHOD OF PAYMENT

No fee is required.


The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC.  
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Chicago, Illinois 60606  
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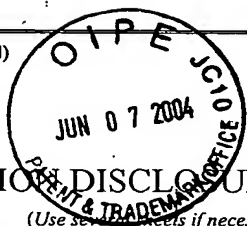
By:

  
Mark C. Zimmerman  
Registration No.: 44,006

June 2, 2004

Attorney for Intel Corporation

Form PTO-1449 (Modified)

U.S. Department of Commerce  
Patent and Trademark Office

Atty. Docket No.

Intel/18683

Serial No.

10/807,668

## INFORMATION DISCLOSURE STATEMENT

(Use separate sheets if necessary)

Applicants

Zimmer et al.

Filing Date

3/23/04

Group Art Unit

Unknown

## 1. U.S. PATENT DOCUMENTS

| *EXAMINER<br>INITIALS | DOCUMENT<br>NUMBER | ISSUE<br>DATE | NAME | CLASS | SUBCLASS | FILING DATE<br>IF<br>APPROPRIATE |
|-----------------------|--------------------|---------------|------|-------|----------|----------------------------------|
|                       |                    |               |      |       |          |                                  |
|                       |                    |               |      |       |          |                                  |

## FOREIGN PATENT DOCUMENTS

| *Examiner<br>Initials | Document<br>Number | Publication<br>Date | Country | Class | Subclass | Translation<br>Yes No |
|-----------------------|--------------------|---------------------|---------|-------|----------|-----------------------|
|                       |                    |                     |         |       |          |                       |

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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|--|---|
|  | Kung, Ling-Pei, "Introduction to Error Correcting Codes" Hitachi Advanced Research Laboratory, 1992.  |
|  | "Memory Glossary" [online], [retrieved on 01/31/2004]. CST, Inc, 2004. Retrieved from the Internet <URL: <a href="http://www.simmtester.com/page/memory/show_glossary.asp?E">http://www.simmtester.com/page/memory/show_glossary.asp?E</a> >. |

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.